

Proposed Amendment
U.S. Serial No. 10/687,655
Your Reference: US6673
Our Reference: 0756-7206
September 26, 2007
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Please amend claims 4-6 as follows.

1. (Canceled)
2. (Previously Presented) A display device comprising:
 - a substrate;
 - a pixel portion comprising a plurality of pixels formed over the substrate;
 - a driving circuit formed over the substrate, which controls the pixel portion;
 - a charge pump circuit which supplies a voltage to the driving circuit, the charge pump circuit comprising:
 - a switching element formed over the substrate; and
 - a capacitor; and
 - a charge pump control circuit formed over the substrate, which controls the charge pump circuit, the charge pump control circuit comprising:
 - a variable frequency-dividing circuit formed over the substrate comprising:
 - a plurality of dividing circuits formed over the substrate;
 - a plurality of switches electrically connected to the plurality of dividing circuits; and
 - a decoder for controlling the plurality of switches; and
 - a processing circuit formed over the substrate, which controls the variable frequency-dividing circuit.
3. (Original) A display device according to claim 2, wherein the charge pump control circuit can vary a clock frequency to input to the switching element.
4. (Currently Amended) A display device according to ~~claim 3~~ claim 2, wherein the clock frequency is controlled by ~~a CPU~~ the processing circuit.

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5. (Currently Amended) A display device according to ~~claim 4~~ claim 2, wherein ~~the CPU the processing circuit is comprised of~~ comprises a thin film transistor.

6. (Currently Amended) A display device comprising:
a pixel portion comprising a plurality of pixels formed over a substrate;
a charge pump circuit;
a charge pump control circuit formed over the substrate, which controls
the charge pump circuit, the charge pump control circuit comprising:

a variable frequency-dividing circuit ~~comprising including~~ a first thin
film transistor formed over the substrate, the variable frequency-dividing circuit
comprising:

a plurality of dividing circuits formed over the substrate;
a plurality of switches electrically connected to the plurality of
dividing circuits; and

a decoder for controlling the plurality of switches; and
a CPU comprising a second thin film transistor formed over the substrate,
wherein the variable frequency-dividing circuit is controlled by the CPU,
and

wherein a dividing ratio is varied according to a display mode.

7.-9. (Canceled)

10. (Original) A display device according to claim 2, wherein the display device is a liquid crystal display device.

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11. (Original) A display device according to claim 6, wherein the display device is a liquid crystal display device.

12.-13. (Canceled)

14. (Original) A display device according to claim 2, wherein the display device is an EL display device.

15. (Original) A display device according to claim 6, wherein the display device is an EL display device.

16.-17. (Canceled)

18. (Original) A display device according to claim 2, wherein the display device is applied to an electrical equipment selected from the group consisting of a digital camera, a notebook type personal computer, a PDA, a DVD playback device, a folding portable information device, a watch type display device and a mobile phone.

19. (Original) A display device according to claim 6, wherein the display device is applied to an electrical equipment selected from the group consisting of a digital camera, a notebook type personal computer, a PDA, a DVD playback device, a folding portable information device, a watch type display device and a mobile phone.

20. (Canceled)

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21. (Previously Presented) A display device according to claim 2, wherein the switching element is a transistor.

22. (Previously Presented) A display device according to claim 21, wherein the transistor is a thin film transistor.

23. (Previously Presented) A display device according to claim 2, wherein the switching element is a diode.

24. (Previously Presented) A display device according to claim 23, wherein the diode is a PIN diode.

25. (Canceled)

26. (Previously Presented) A display device according to claim 6, wherein the variable frequency-dividing circuit further comprises a plurality of dividing circuits, a plurality of switches electrically connected to the plurality of dividing circuits, and a decoder for controlling the plurality of switches.